

FIG. 1

2/10

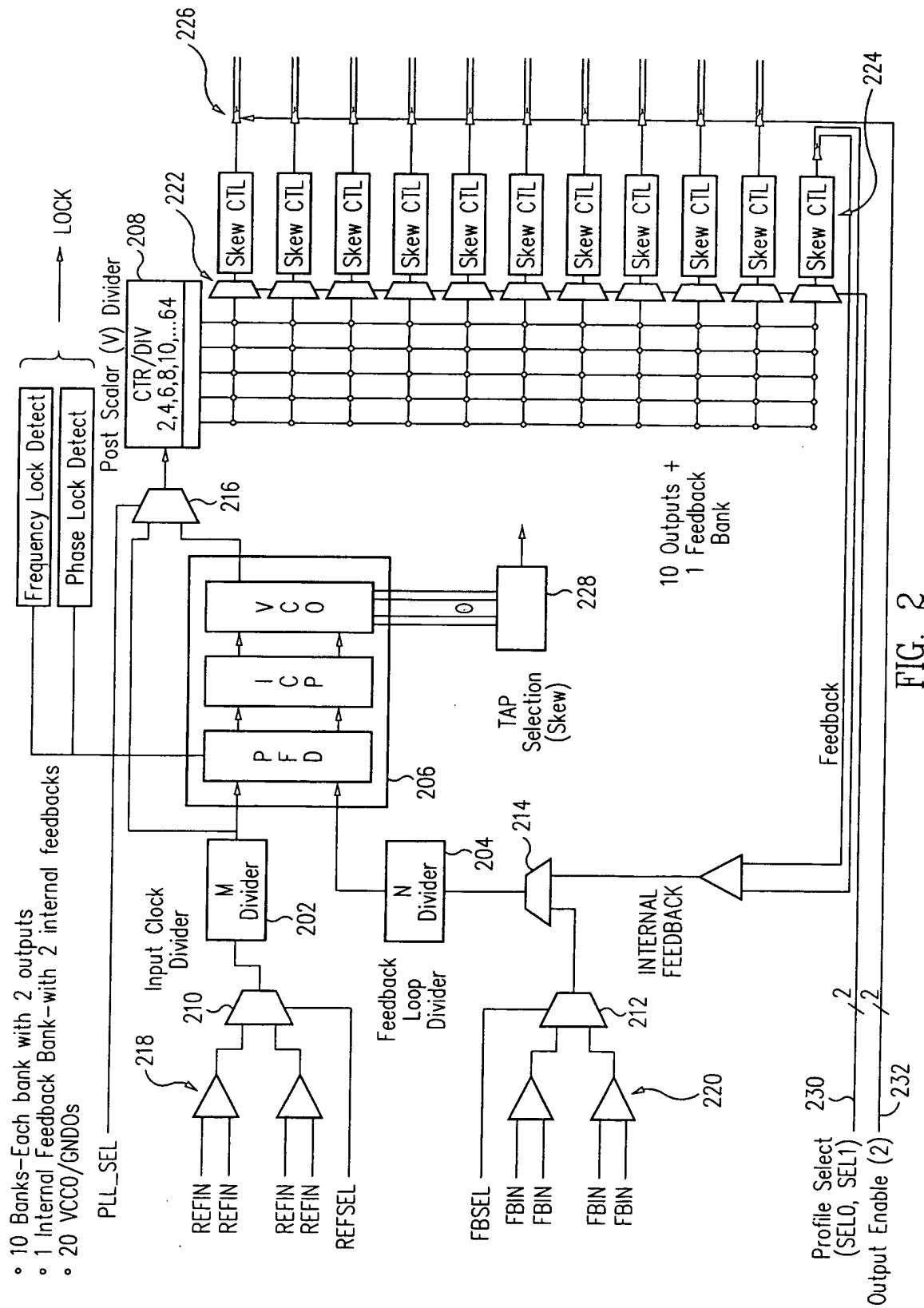


FIG. 2



FIG. 3

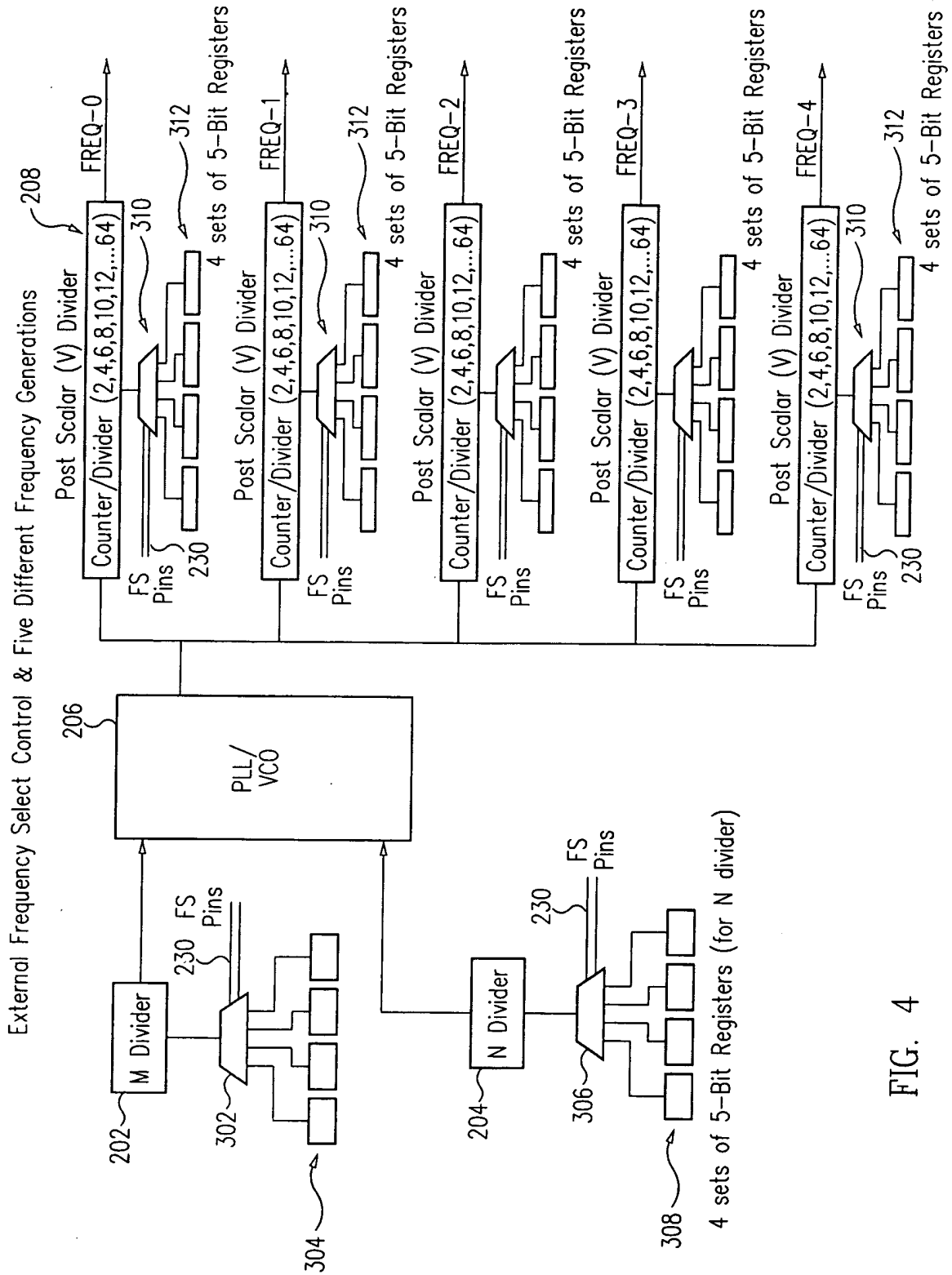


FIG. 4

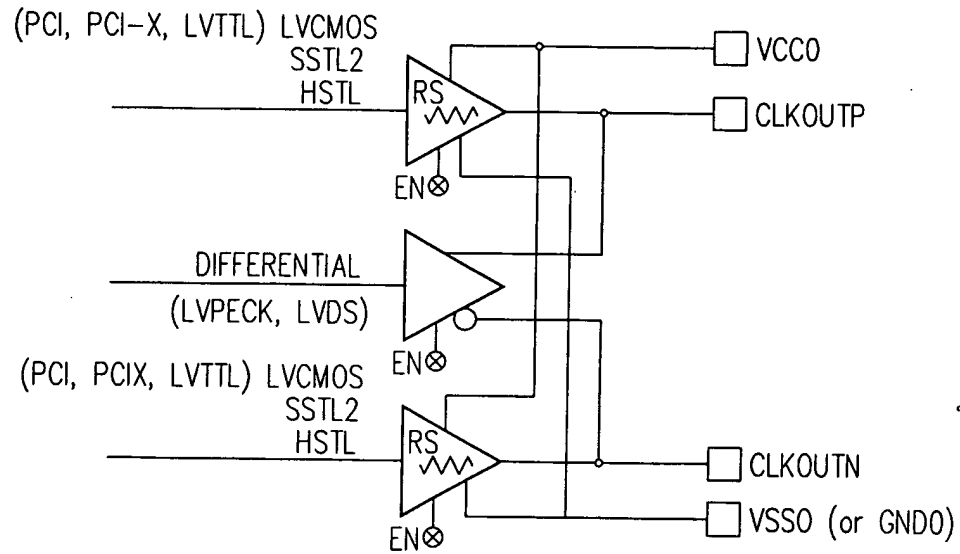


FIG. 5

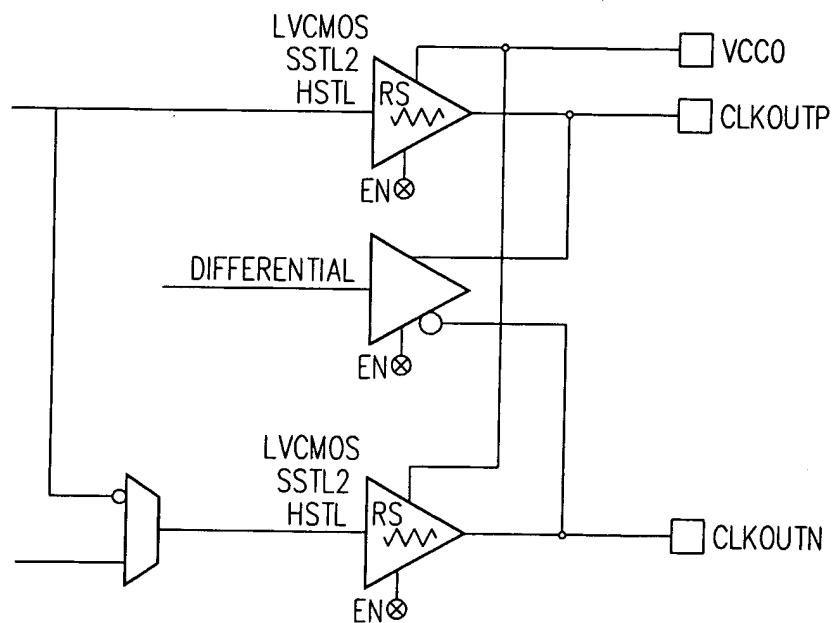


FIG. 6

6/10

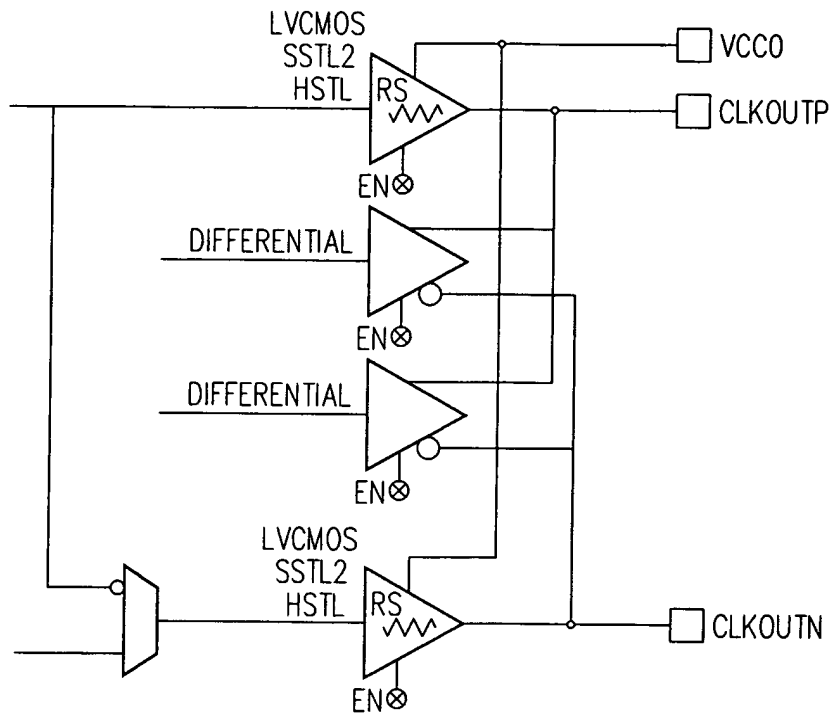


FIG. 7

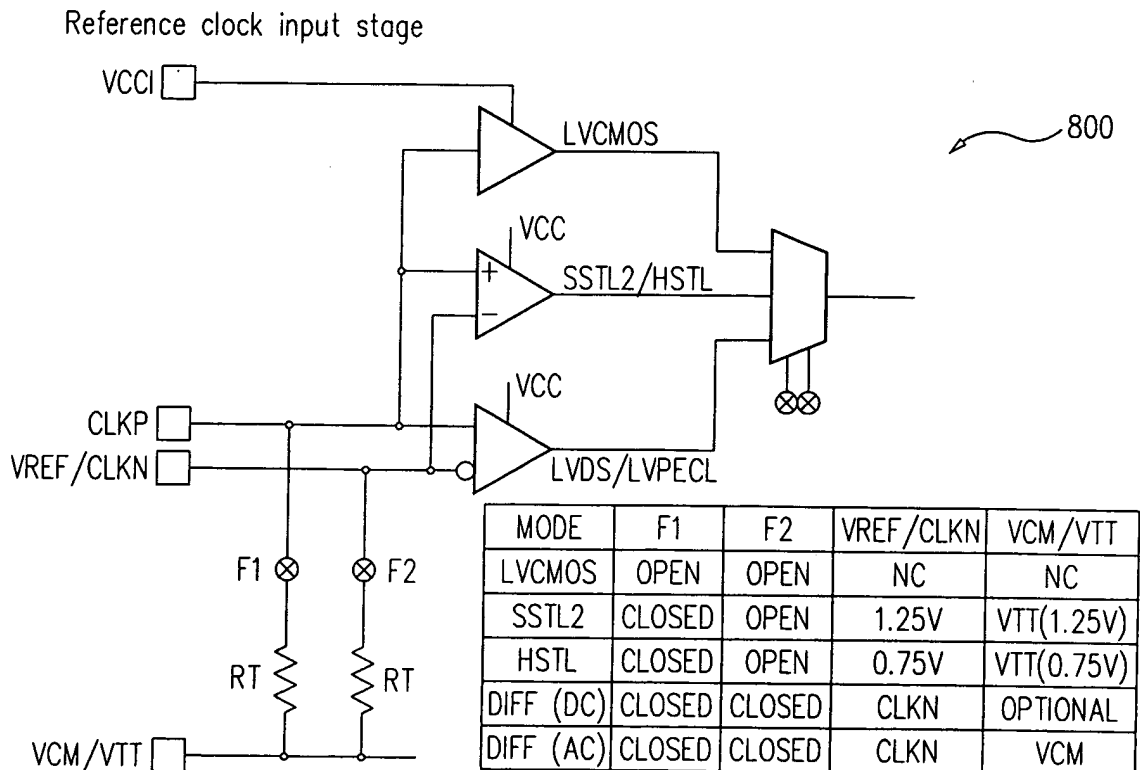


FIG. 8

SSTL2/HSTL input application

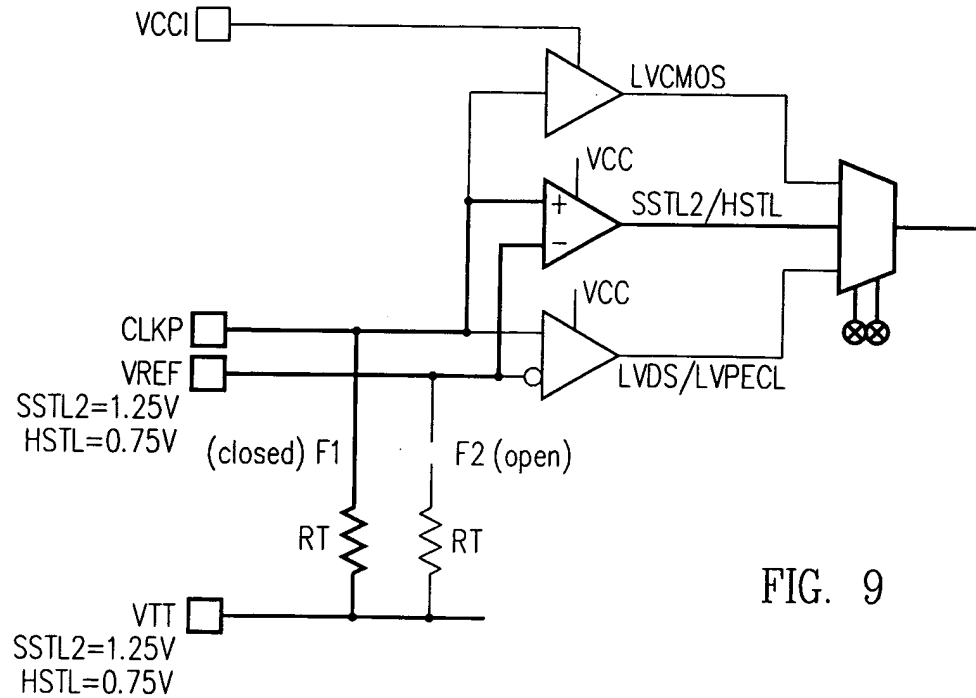


FIG. 9

Alternative DC coupled differential application

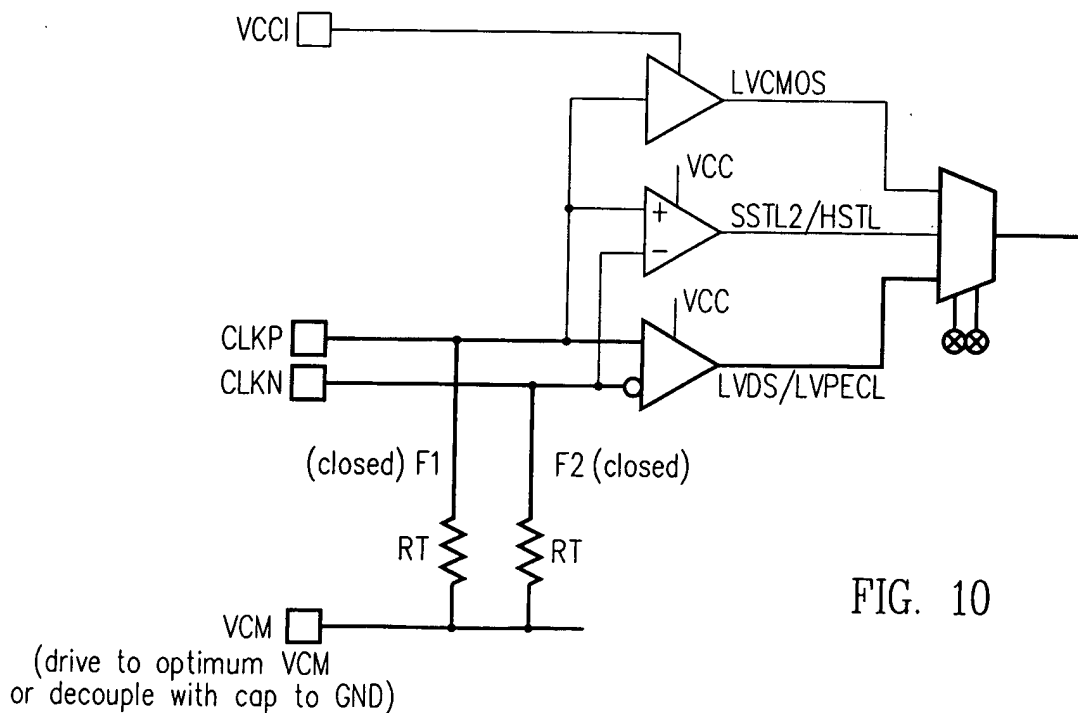


FIG. 10

(drive to optimum VCM
or decouple with cap to GND)

8/10

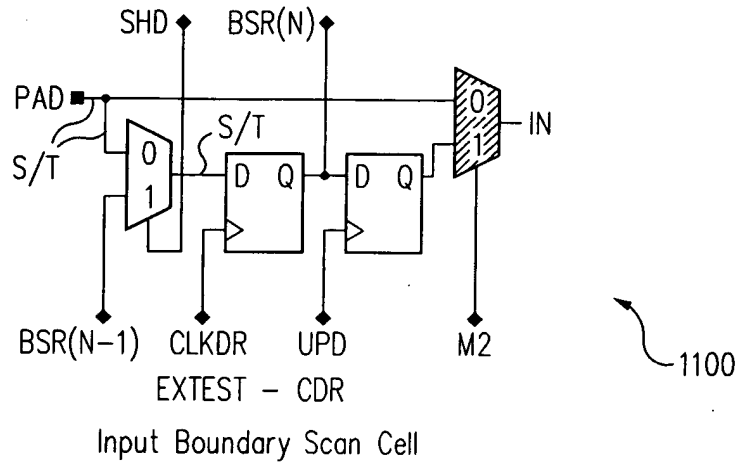


FIG. 11

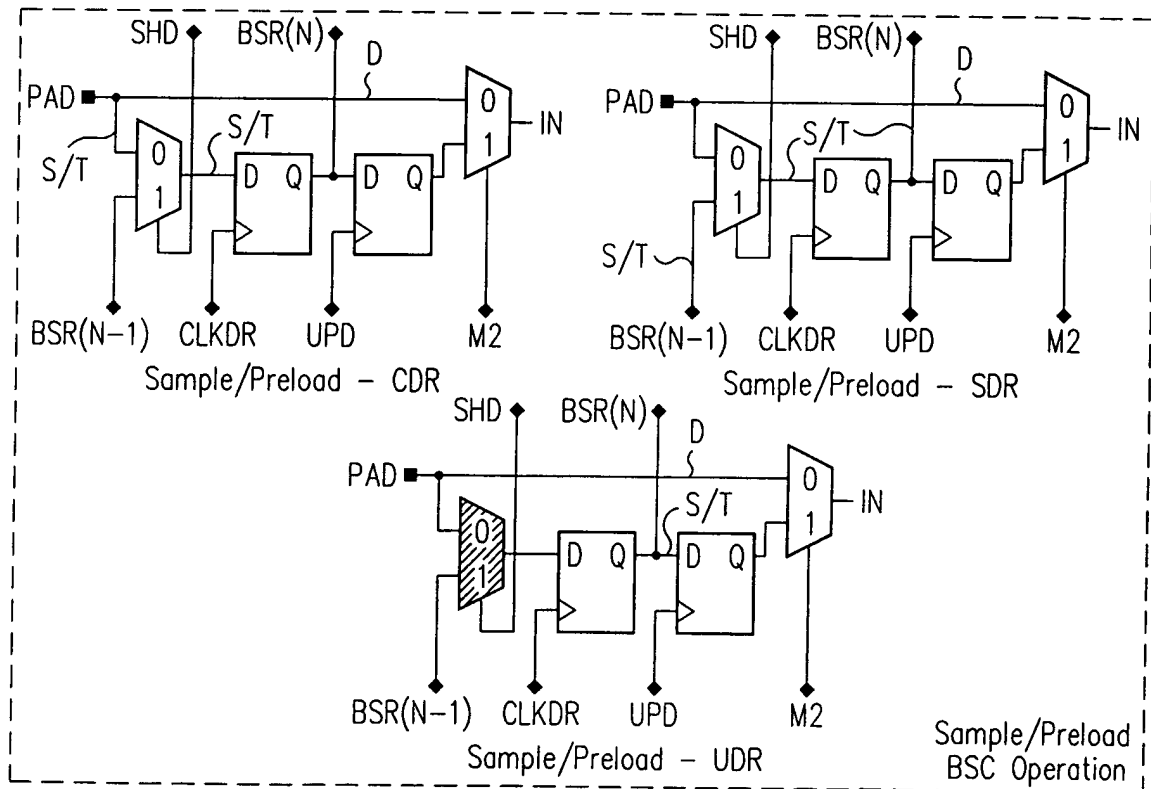


FIG. 12

9/10

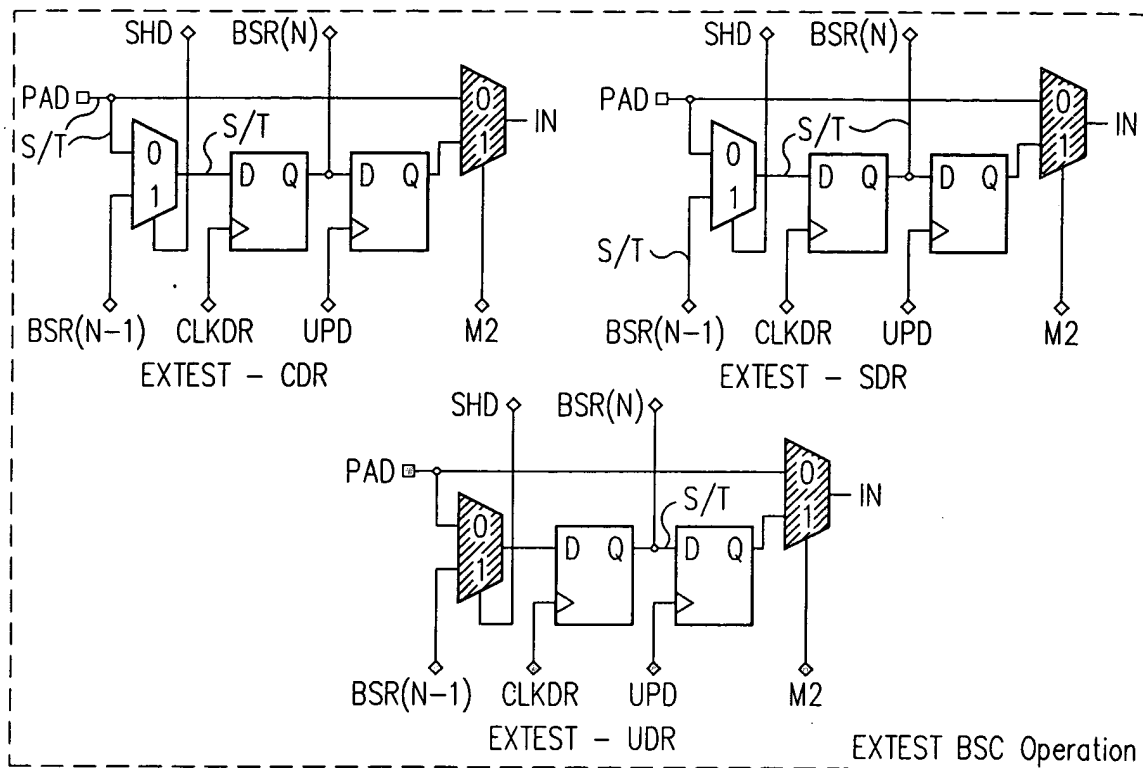


FIG. 13

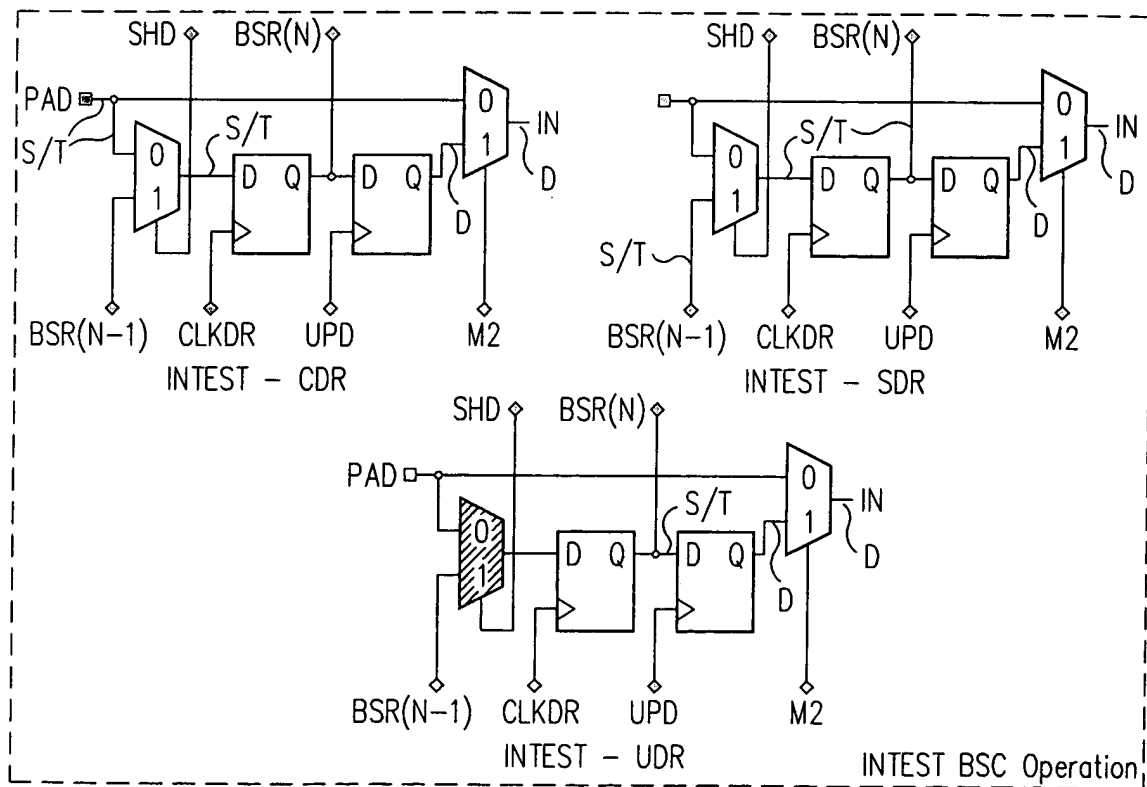


FIG. 14

10/10

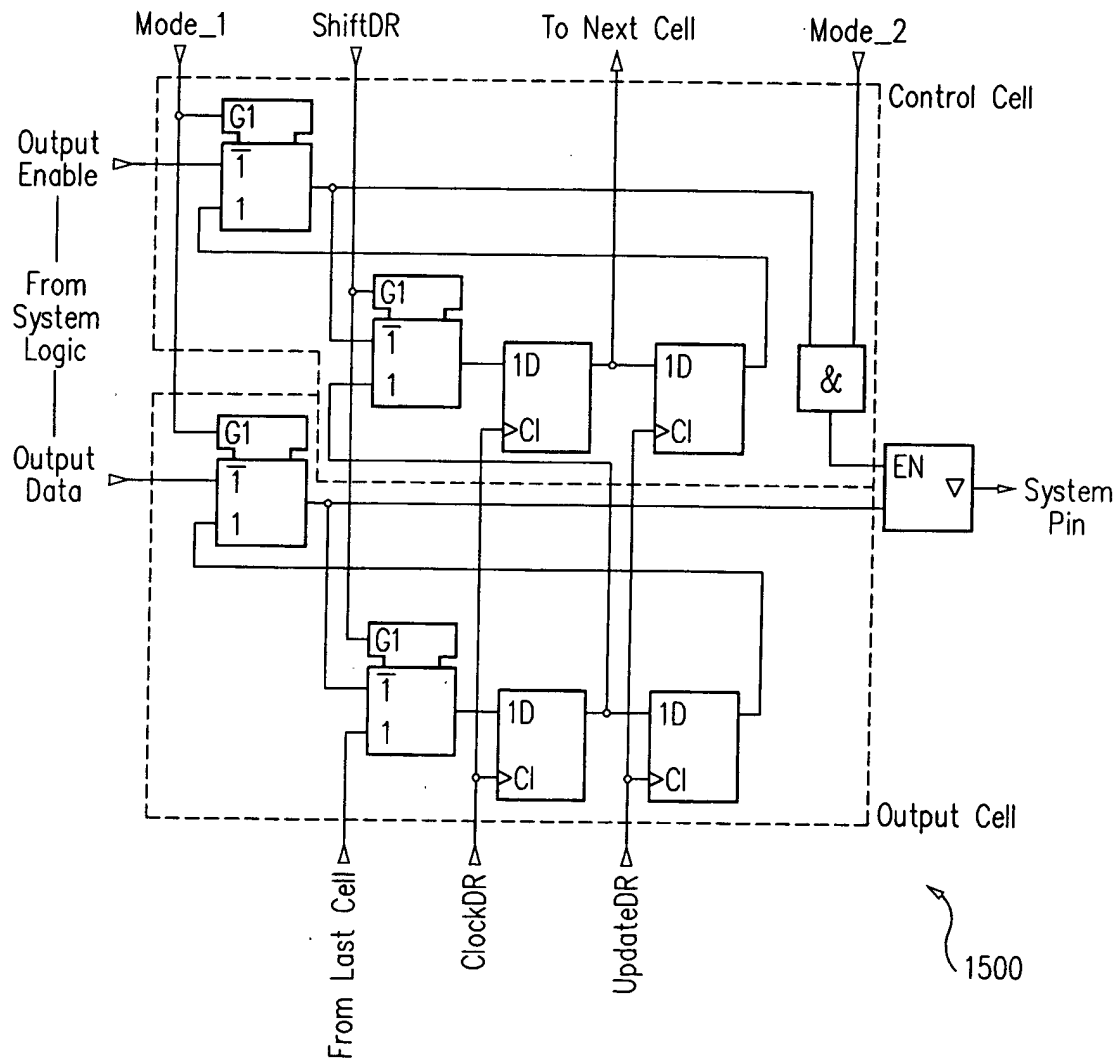


FIG. 15